

	Subclass	ISSUE CLASSIFICATION
	Class	

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**PATENT NUMBER**

**U.S. UTILITY Patent Application**

PD O.I.P.E. PATENT DATE  
SCANNED RCO Q.A. CS /

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/506158	F	220 713	190	<del>273P</del> 2134	Adams

**E** Circuit and method for the system of claim 1, characterized in that the system is a single chip.

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner) _____ (Date)				
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